



FIG. 1A
(Related Art)

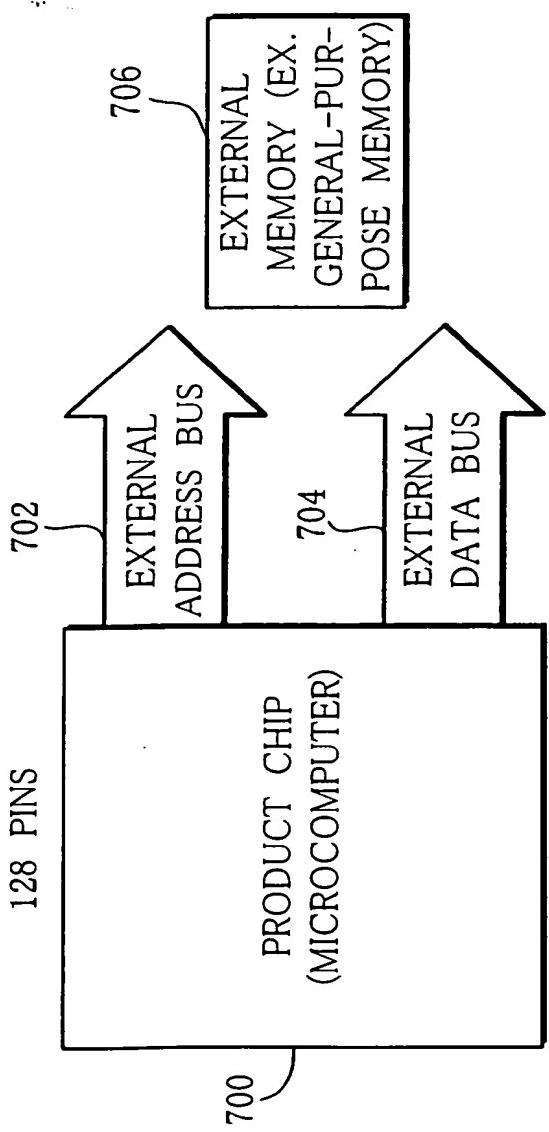
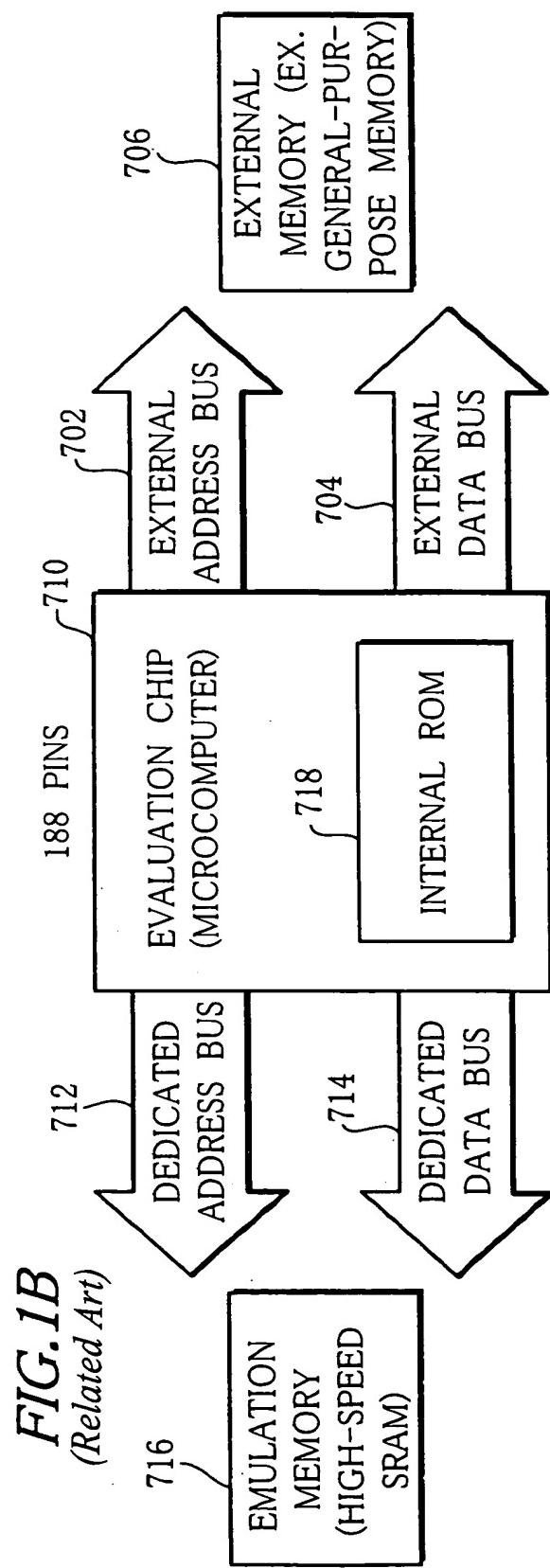


FIG. 1B
(Related Art)



O I P E
JUN 16 2004
U.S. PATENT & TRADEMARK OFFICE
LIV

FIG.2

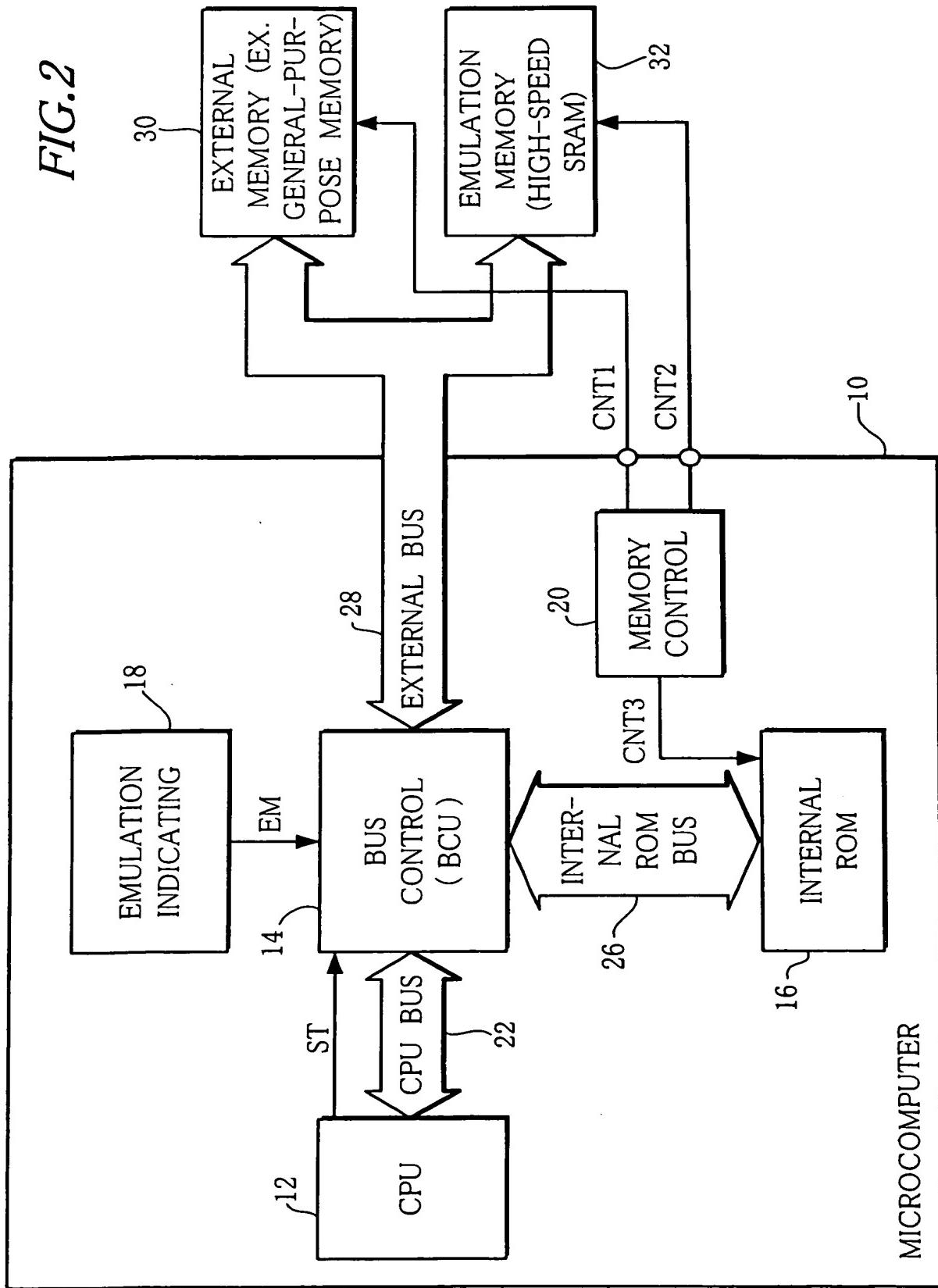




FIG.3

FIG.3A

FIG.3B

FIG.3A

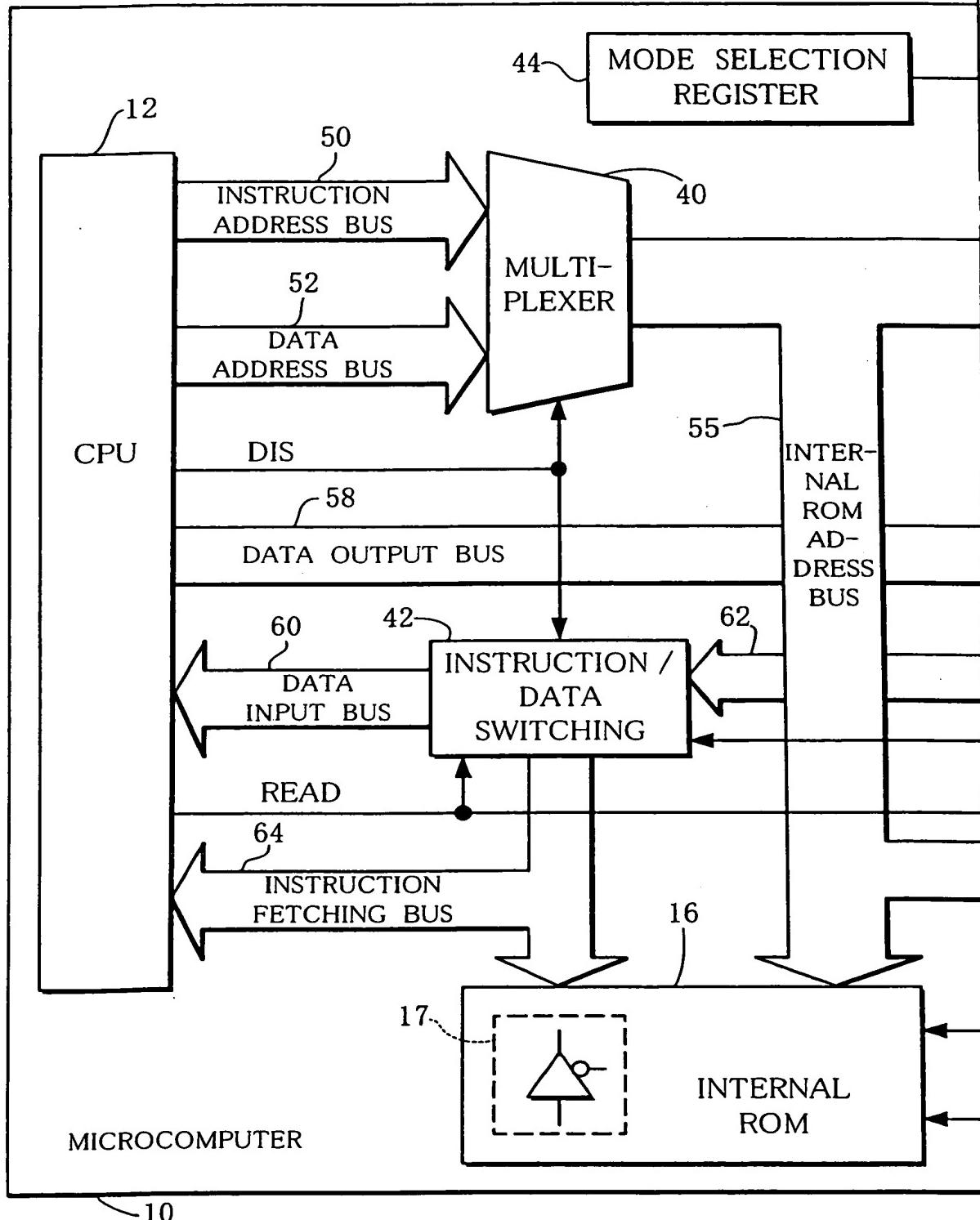




FIG. 3B

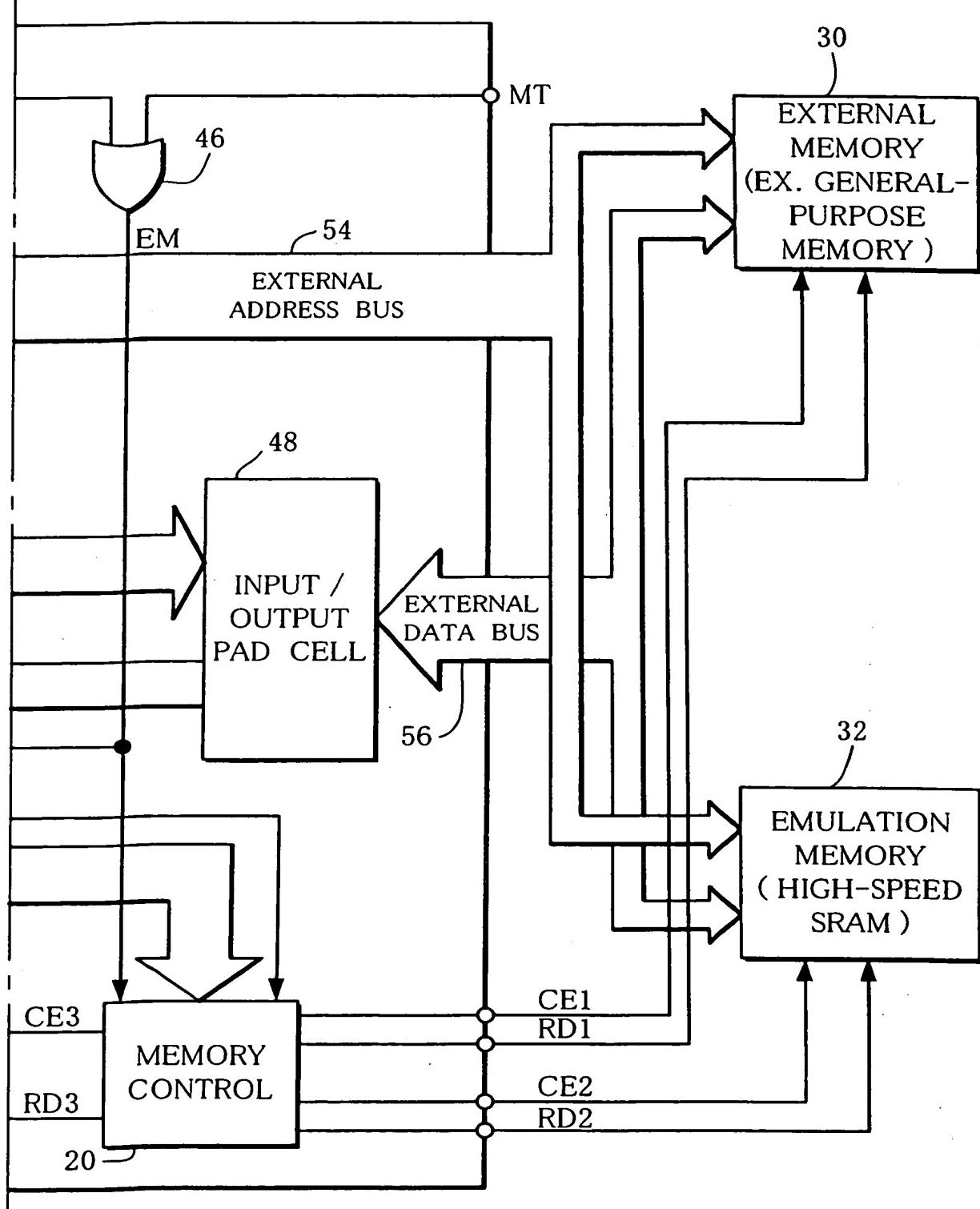




FIG. 4

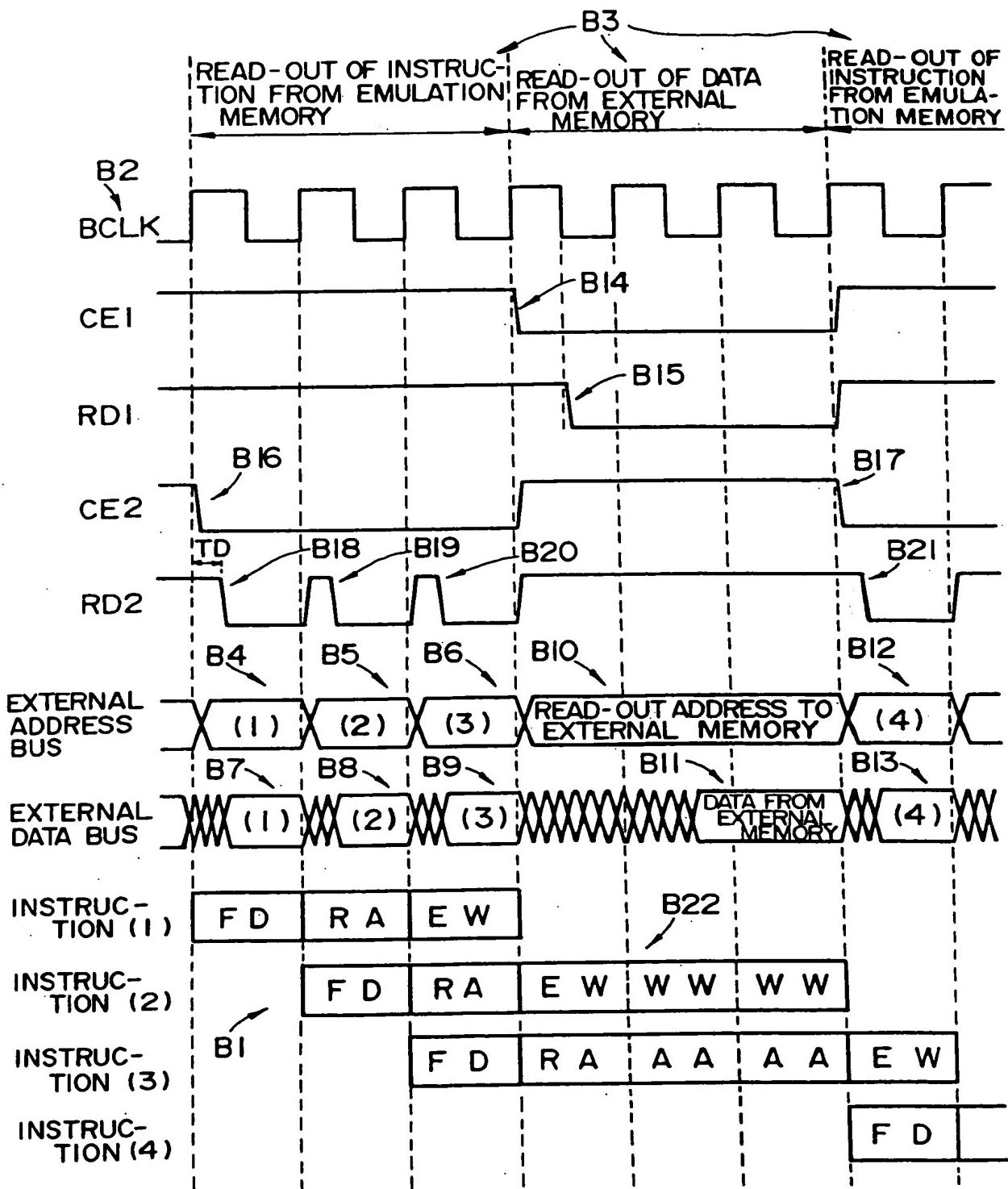


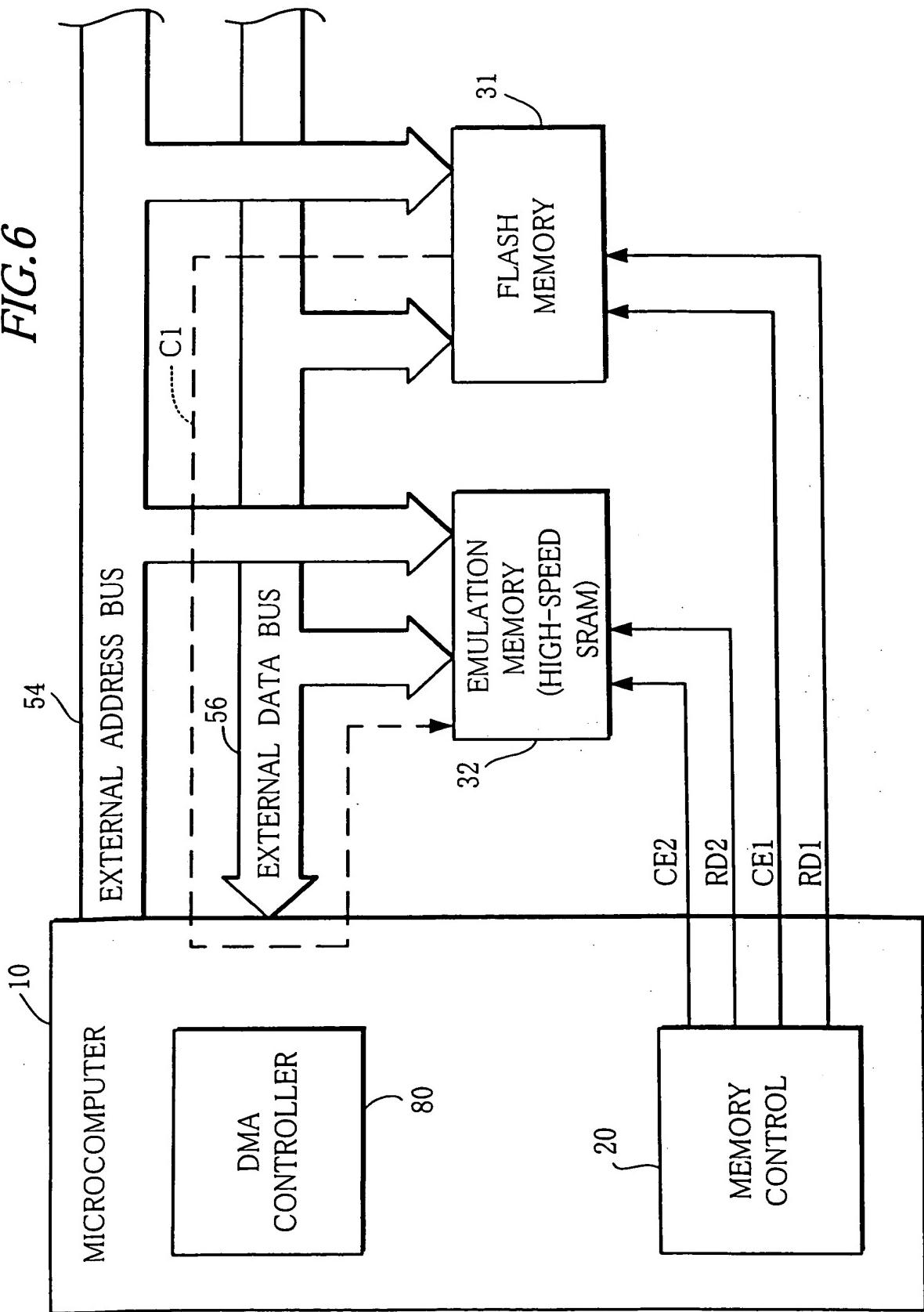


FIG. 5

TERMINAL MT	
0 0	BOOTING FROM EMULATION MEMORY
0 1	BOOTING FROM EMULATION MEMORY AFTER INFORMATION HAS BEEN TRANSFERRED TO EMULATION MEMORY (OTP MODE)
1 0	BOOTING FROM INTERNAL ROM
1 1	BOOTING FROM EXTERNAL MEMORY

O 1 r n
JUN 16 2004
PATENT & TRADEMARK OFFICE
U.S. GOVERNMENT

FIG. 6



O I P E
JUN 16 2004
U.S. PATENT AND TRADEMARK OFFICE

FIG. 7A

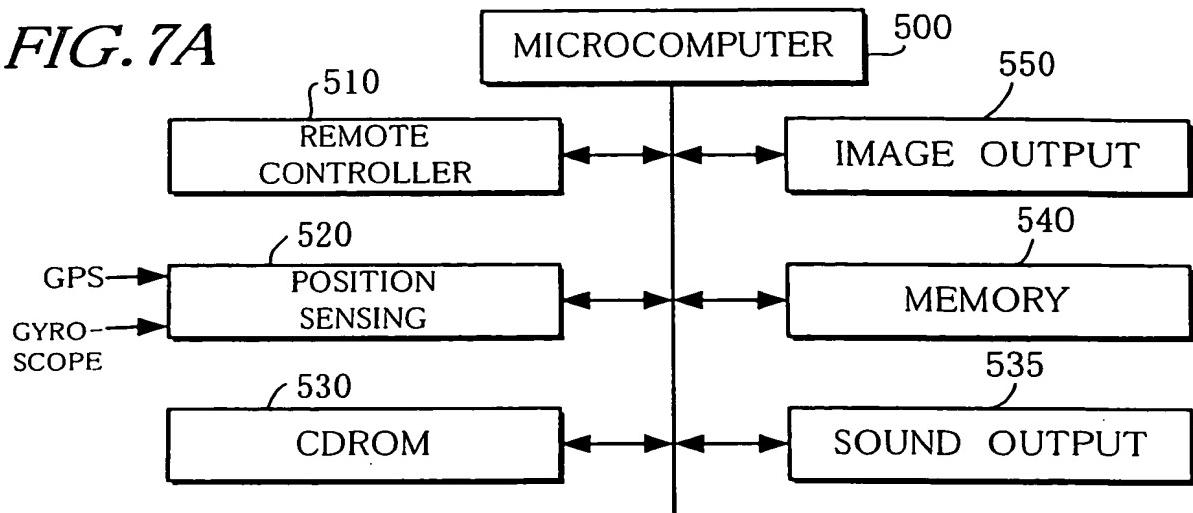


FIG. 7B

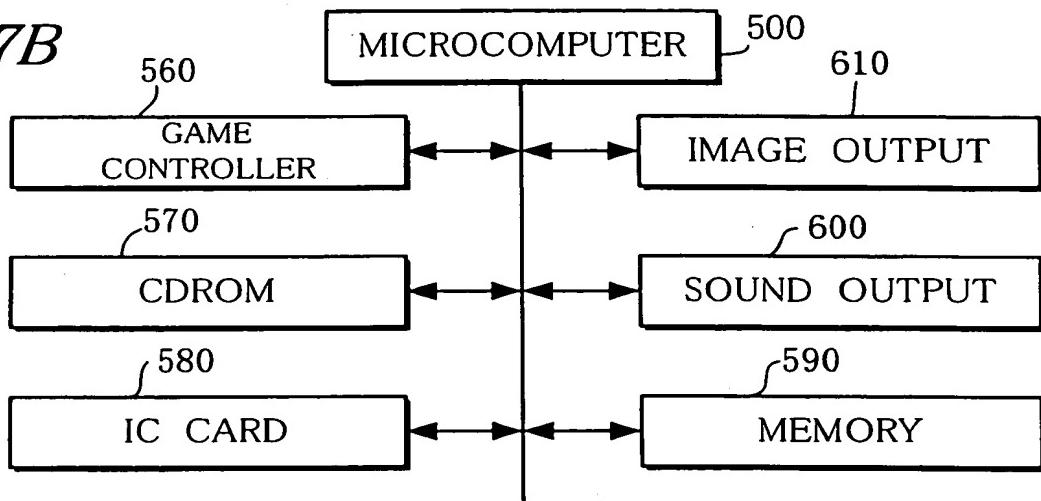


FIG. 7C

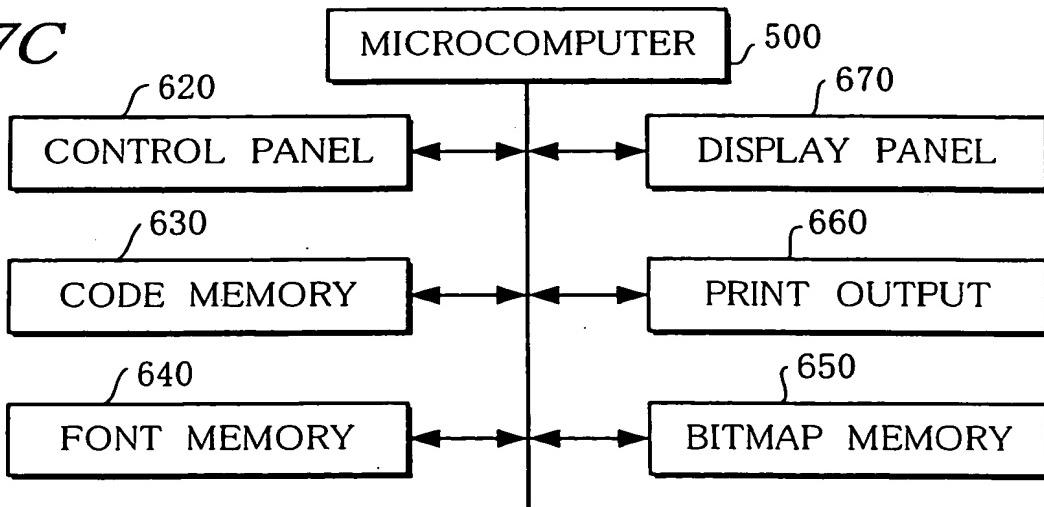




FIG. 8A

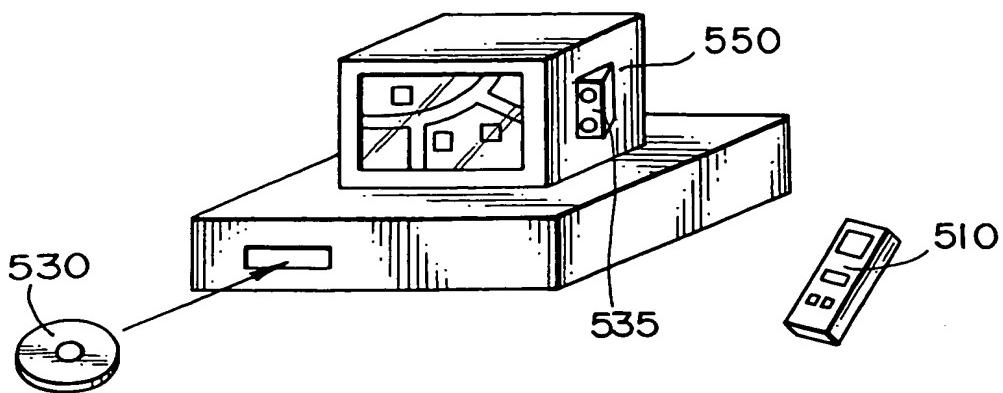


FIG. 8B

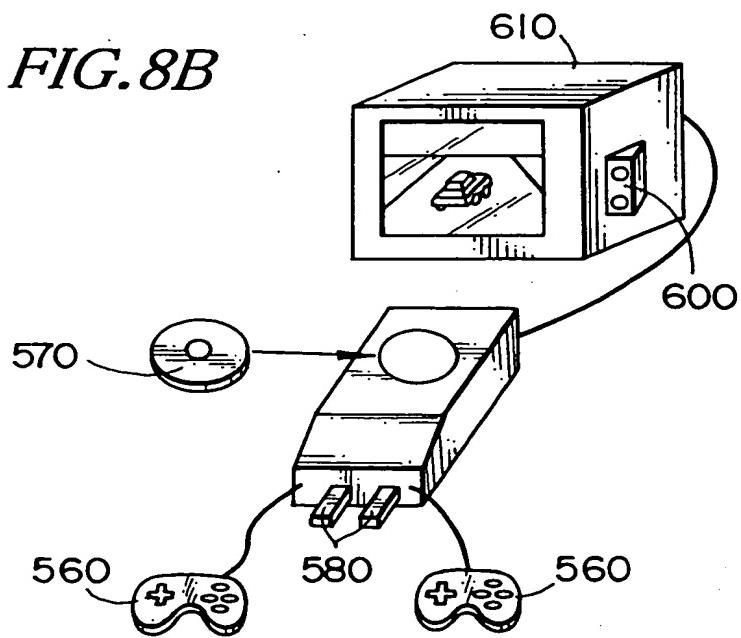


FIG. 8C

